

## HIGH FREQUENCY SIGNAL PEAK DETECTOR

### ABSTRACT OF THE DISCLOSURE

5           A signal power detector includes an input coupling circuit and a rectifying  
operational amplifier. The input coupling circuit is operably coupled to receive a signal  
and to convert the signal into a first input and a rectifying input. The rectifying  
operational amplifier is operably coupled to receive the first input and the rectifying input  
and to produce therefrom a rectified output signal that represents a peak of the received  
10   signal.